

PATENT
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By: _____

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

RICHARD G. CLIFF et al.

Application No.: Unassigned

Filed: Herewith

For: PROGRAMMABLE LOGIC
ARRAY INTEGRATED CIRCUITS

Examiner: Unassigned

Art Unit: Unassigned

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination of the above-referenced application, please enter the following amendments and remarks.

IN THE SPECIFICATION:

Please replace the paragraph beginning page 1, line 5 with the following:

--This patent application is a continuation of

U.S. patent application 09/496,945, filed February 3, 2000, which is a continuation of

U.S. patent application 09/179,254, filed October 26, 1998, which is a continuation of

U.S. patent application 08/851,858, filed May 6, 1997, which is a continuation of

U.S. patent application 08/655,870, filed May 24, 1996, which is a continuation of

U.S. patent application 08/245,509, filed May 18, 1994, which is a continuation-in-part of

U.S. patent application 08/111,693, filed August 25, 1993, which is a continuation-in-part of

U.S. patent application 07/880,942, filed May 8, 1992, and

U.S. patent application 07/754,017, filed September 3, 1991.

which are all incorporated by reference.--